



8/Ext(1)
5/15/03

PATENT
51270-277019

Smith

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Tsuji, et al.

Serial No: 09/982,335

Filed: October 18, 2001

For: SEMICONDUCTOR INPUT
PROTECTION CIRCUIT

Art Unit: 2811

Examiner: Ori Nadav

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:
Assistant Commissioner for Patents
Washington D.C. 20231, on

May 5, 2003

Date of Deposit

Mark R. Kendrick

Name

Mark R. Kendrick

Signature

5/5/03

Date

PETITION FOR EXTENSION OF TIME

Asst. Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In accordance with Rule 136, Applicant respectfully petitions the Commissioner for a 1-month extension of time extending to May 6, 2003, the period for response to the Office Action dated January 6, 2003. A check for \$110.00 and the responsive paper(s) are attached.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 16-1805. A copy of this petition is enclosed.

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Respectfully submitted,

PILLSBURY WINTHROP LLP

By:

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Date: May 5, 2003

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